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Substitute for form 1449/PTO		Complete if Known	
INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use as many sheets as necessary)		Application Number	101781,112
		Filing Date	February 18, 2004
		First Named Inventor	Wu, Koucheng
		Art Unit	2818
		Examiner Name	
Sheet 1 of 1	Attorney Docket Number	KWU002	

U. S. PATENT DOCUMENTS					
Examiner Initials*	Cite No. ¹	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code ² (if known)			
TH		US- 5,796,142	8/18/1998	Ruei-Ling Lin	
		US- 5,885,868	5/23/1999	Ruei-Ling Lin	
		US- 5,995,412	11/30/1999	Yoshiji Ohta	
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FOREIGN PATENT DOCUMENTS						
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Examiner Signature	TH TH HO	Date Considered	July 2005
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NON PATENT LITERATURE DOCUMENTS				
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²	
TH	1	D. Burnett, D. Shum, and K. Baker, "An Advanved Flash Memory Technology on SOI",		
		IEDM Tech. Digest, pp. 983-986, 1998.		
TH	2	K. S. Kim, J. Y. Kim, J. W. Yoo, Y. B. Choi, M. K. Kim, B.Y. Nam, K. T. Park, S. T. Ahn,		
		and O. H. Kwon, "A Novel Dual String NOR (DuSNOR) Memory Cell Technology Scalable to the 256 Mbit		
		and 1 Gbit Flash Memories", IEDM Tech. Digest, pp. 263-266, 1995.		
TH	3	Y. Yamauchi, M. Yoshimi, S. Sato, H. Tabuchi, N. Takenaka, and K. Sakiyam, "A New Cell Structure		
		for Sub-quarter Micron High Density Flash Memory", IEDM Tech. Digest, pp. 267-270, 1995.		
TH	4	S. Aritome, R. Shiota, R. Kirisawa, T. Endoh, R. Nakayama, K. Sakui, and F. Masuoka,		
TH		"A Reliable Bi-polarity Write/Erase Technology in Flash EEPROMs", IEDM Tech. Digest, pp. 111-114,		
		1990.		

Examiner Signature	TH-TH 110	Date Considered	July 2005
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